S26	9	logic same design and (fan-in "fan in") same display	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/08/18 08:54
S99	2	"5220512".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/01 08:37
S100	14	(US-20020147576-$).did. or (US-5111413-$ or US-5220512-$ or US-5461574-$ or US-5751592-$ or US-5867399-$ or US-5933633-$ or US-5937190-$ or US-5991533-$ or US-6029262-$ or US-6324678-$ or US-6442741-$ or US-6470482-$ or US-6643836-$).did.	US-PGPUB; USPAT	OR	OFF	2005/03/01 13:07
S101	32	(waveform adj (debugging tracing))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/02 10:03
S102	14	(sequential adj circuit) same debug$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/02 10:07
S107	228	("703".clas. "716".clas. "347".clas. "715".clas.) and (state adj table)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/02 10:45
S108	100	S107 and (clock pulse)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/02 10:35
S109	24	S108 and (graphical same display)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/02 10:41
S110	16	S108 and (digital adj logic)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/02 10:45

S11 1	322	("703".clas. "716".clas. "345".clas. "715".clas.) and (state adj table)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/02 10:45
S11 2	143	S111 and (clock pulse)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/02 10:45
S11 3	38	S112 and (graphical same display)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/02 10:45
S11 4	14	S113 not S109.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/02 10:47
S11 5	17	S112 and (digital adj logic)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/02 10:47
S11 6	1	S115 not S110	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/02 10:47
S11 7	824	("703".clas. "716".clas. "345".clas. "715".clas.) and (digital adj logic)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/02 10:48
S11 8	1079	("703".clas. "716".clas. "345".clas. "715".clas.) and ((digital sequential) adj logic)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/02 10:48
S11 9	185	S118 and (state same display)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/02 10:48
S12 0	31	S119 and (state same display same register)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/02 11:19

S12 1	887	345/440.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/02 11:19
S12 2	10	S121 and (digital adj logic)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/02 11:21
S12 3	16	S121 and (state adj diagram)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/02 11:21
S12 4	7	S121 and (state adj table)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/02 13:22
S12 5	623	716/11.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/02 13:22
S12 6	14	S125 and (state adj (table diagram))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/02 13:32
S12 7	19	S125 and (state adj (transition))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/02 13:32
S12 8	16	S127 not S126	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/02 13:40
S12 9	0	716/11.ccls. and ((sequential clocked) adj circuit) and (waveform adj display)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/02 13:41
S13 0	1	716/11.ccls. and ((sequential clocked) same circuit) and (waveform adj display)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/02 13:41

S131	1	716/11.ccls. and ((sequential clocked) same logic) and (waveform adj display)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/02 13:42
S132	8	716/11.ccls. and ((sequential clocked) same (circuit logic)) and (waveform)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/02 13:42
S133	9	("4587625" | "4675832" | "4791593" | "5220512" | "5381524" | "5555201" | "5615356" | "5949690" | "6029262").PN. OR ("6789242").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/03/02 14:17